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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group
Art Unit:

2827

Attorney

Docket No.:

121056-020

Applicant:

Hiroshi KIMURA

Invention:

SEMICONDUCTOR DEVICE, ITS

MANUFACTURING METHOD AND ELECTRO DEPOSITION FRAME

Serial No:

09/837,022

Filed:

April 18, 2001

Examiner:

James Mitchell

## Certificate Of Mailing Under 37 CFR 1.8(a)

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on \_\_\_\_ June 16, 2004

Marriynn M. Peterson

## SUBMISSION OF CORRECTED FORMAL DRAWING

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

In response to the Notice of Allowability mailed May 21, 2004, Applicant is submitting herewith a corrected formal drawing of Figure 8 which includes the "prior art" label requested by the Examiner on May 23, 2002. A Request For Approval of Drawing Amendments was previous submitted to the Examiner on August 20, 2002. The corrected formal drawing of Fig. 8 is on a

"Replacement Sheet" that includes Figs. 6A, 6B and 7 which have not been changed.

Respectfully submitted,

Michael S. Gzybowski

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL (37 CFR 1.8)				Docket No.	
Applicant(s): Hiroshi KIMURA				121056-0020	
Application No. 09/837,022	Filing Date 04/18/2001	Examiner  1 8 2004 Jagues MITCHELL		Customer No. 35684	Group Art Unit 2827
Invention: SEMICONDUCTOR D	W. A. D.	URING METHOD AND ELECTRO	DE	POSITION FR	AME
. I hereby certify that	this <u>Submission of Corre</u>	cted Formal Drawing (Identify type of correspondence)	<b>\</b>		
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